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03/04/02

U.S. UTILITY Patent Application

PATENT NUMBER and  
ISSUE DATE

APPL NUM 10086892	FILING DATE 03/04/2002	CLASS 438	SUBCLASS 1 +	GAU 2814	EXAMINER 2813 HOGAN
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\*\*APPLICANTS: Lee Kyo-Yeol;

\*\*CONTINUING DATA VERIFIED: *DA*  
*None*

\*\* FOREIGN APPLICATIONS VERIFIED: *DA*  
REPUBLIC OF KOREA 2001-11153 03/05/2001

PG-PUB DO NOT PUBLISH ☐

RESCIND ☐

Foreign priority claimed ☒ yes ☐ no  
35 USC 119 conditions met ☒ yes ☐ no  
Verified and Acknowledged Examiners's initials *DA*

ATTORNEY DOCKET NO  
030681-366

TITLE : Method for fabricating group III-V compound semiconductor substrate

U.S. DEPT. OF COMM./PAT. & TM.-PTO-436L (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G.
Assistant Examiner		DRAWING	
		Sheets Drwg.	Figs. Drwg.
ISSUE FEE		Print Fig.	
Amount Due	Date Paid		
Primary Examiner		Application Examiner	
PREPARED FOR ISSUE			
<input type="checkbox"/> TERMINAL DISCLAIMER		WARNING: The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368, Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.	

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